

8/9/04

10/066833

L Number	Hits	Search Text	DB	Time stamp
-	1	("20020116601").PN.	USPAT; US-PGPUB	2004/07/28 09:59
-	568652	pipeline processor microprocessor cpu	USPAT; US-PGPUB	2004/08/09 13:15
-	751179	stack status flush "return address" push pop "save state"	USPAT; US-PGPUB	2004/07/30 11:12
-	2264	"instruction bundle" vliw "very long instruction word" "instruction package"	USPAT; US-PGPUB	2004/07/29 09:04
-	1323490	hard\$code\$ embedded cache memory stored ram rom	USPAT; US-PGPUB	2004/07/29 09:09
-	10001	((interrupt function exception disrruption instruction) near call)	USPAT; US-PGPUB	2004/07/29 09:10
-	33273	((code state context function) near switch)	USPAT; US-PGPUB	2004/07/29 09:11
-	195881	(pipeline processor microprocessor cpu) and (stack status flush "return address" push pop "save state")	USPAT; US-PGPUB	2004/07/29 09:12
-	14126	(pipeline processor microprocessor cpu) and ((code state context function) near switch))	USPAT; US-PGPUB	2004/07/29 09:12
-	8310	(pipeline processor microprocessor cpu) and (((interrupt function exception disrruption instruction) near call))	USPAT; US-PGPUB	2004/07/29 09:12
-	7844	((pipeline processor microprocessor cpu) and (stack status flush "return address" push pop "save state")) and ((pipeline processor microprocessor cpu) and (((code state context function) near switch)))	USPAT; US-PGPUB	2004/07/29 09:13
-	557	((pipeline processor microprocessor cpu) and (stack status flush "return address" push pop "save state")) and ((pipeline processor microprocessor cpu) and (((code state context function) near switch)))	USPAT; US-PGPUB	2004/07/29 09:15
-	660	((pipeline processor microprocessor cpu) and (((interrupt function exception disrruption instruction) near call))) and (((interrupt function exception disrruption instruction) near call)) and (((code state context function) near switch))	USPAT; US-PGPUB	2004/07/29 09:17
-	11	((interrupt function exception disrruption instruction) near call)) near (((code state context function) near switch))	USPAT; US-PGPUB	2004/07/29 09:15
-	6925	((interrupt function exception disrruption instruction) near call)) and handl\$	USPAT; US-PGPUB	2004/07/29 09:18
-	145	((interrupt function exception disrruption instruction) near call)) near handl\$	USPAT; US-PGPUB	2004/07/29 09:18
-	8385	"stack status" (pipeline near flush) "return address" "save state" "return state"	USPAT; US-PGPUB	2004/07/29 09:29
-	5853	("stack status" (pipeline near flush) "return address" "save state" "return state") and (pipeline processor microprocessor cpu)	USPAT; US-PGPUB	2004/07/29 16:27
-	1223	((("stack status" (pipeline near flush) "return address" "save state" "return state") and (pipeline processor microprocessor cpu)) and (((interrupt function exception disrruption instruction) near call)))	USPAT; US-PGPUB	2004/07/29 09:35
-	4055	((("stack status" (pipeline near flush) "return address" "save state" "return state") and (pipeline processor microprocessor cpu)) and handl\$	USPAT; US-PGPUB	2004/07/29 09:35

-	1018	((("stack status" (pipeline near flush) "return address" "save state" "return state") and (pipeline processor microprocessor cpu)) and (((interrupt function exception disrruption instruction) near call))) and handl\$	USPAT; US-PGPUB	2004/07/29 09:45
-	478	((("stack status" (pipeline near flush) "return address" "save state" "return state") and (pipeline processor microprocessor cpu)) and (((interrupt function exception disrruption instruction) near call))) and handl\$) and pipeline	USPAT; US-PGPUB	2004/07/29 09:48
-	478	((interrupt function exception disrruption instruction) near call)) and ("stack status" (pipeline near flush) "return address" "save state" "return state") and handl\$ and pipeline	USPAT; US-PGPUB	2004/07/29 09:50
-	5	((interrupt function exception disrruption instruction) near call)) near handl\$) and ("stack status" (pipeline near flush) "return address" "save state" "return state") and pipeline	USPAT; US-PGPUB	2004/07/29 09:50
-	224	(712/219).CCLS.	USPAT; US-PGPUB	2004/07/29 15:21
-	1	("5867701").PN.	USPAT; US-PGPUB	2004/07/29 16:11
-	1	high-performance and multi-processor and floating and point and unit	EPO	2004/08/09 08:30
-	2	"9918497".	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/07/29 16:28
-	1	("5867701").PN.	USPAT; US-PGPUB	2004/07/30 09:06
-	33	("3911406"   "4038533"   "4050058"   "4199811"   "4342078"   "4363091"   "4376976"   "4422144"   "4542453"   "4610000"   "4654781"   "4751703"   "4875160"   "4965724"   "5003462"   "5005118"   "5101344"   "5129067"   "5134701"   "5168571"   "5202967"   "5210758"   "5212693"   "5233694"   "5235686"   "5292592"   "5307478"   "5335344"   "5371864"   "5452426"   "5463745"   "5542059"   "5546552").PN.	USPAT USPAT	2004/08/09 13:16
-	3	5867701.URPN.	USPAT	2004/07/30 10:37
-	4807	instruction near (insert instertion add pipeline)	USPAT; US-PGPUB	2004/07/30 09:07
-	334	instruction near (insert instertion)	USPAT; US-PGPUB	2004/08/09 14:16
-	176614	(interrupt or function call or exception) and (embed or insert)	USPAT; US-PGPUB	2004/07/30 10:27
-	1247	(interrupt or function call or exception) near (embed or insert)	USPAT; US-PGPUB	2004/07/30 10:28
-	75	(interrupt or function call or exception) near (embed or insert) and pipeline	USPAT; US-PGPUB	2004/07/30 10:28
-	1	5867701.pn.	USPAT	2004/07/30 10:37
-	7794	(embed\$ insert\$) near (code microcod\$ instruction opcode)	USPAT; US-PGPUB	2004/07/30 11:14
-	819	stack near (status flush "return address" "processor state")	USPAT; US-PGPUB	2004/07/30 11:14
-	3411	(embed\$ insert\$) adj (code microcod\$ instruction opcode)	USPAT; US-PGPUB	2004/07/30 11:14
-	94	(stack near (status flush "return address" "processor state")) and ((embed\$ insert\$) adj (code microcod\$ instruction opcode))	USPAT; US-PGPUB	2004/07/30 11:15
-	66	((stack near (status flush "return address" "processor state")) and ((embed\$ insert\$) adj (code microcod\$ instruction opcode))) and pipeline	USPAT; US-PGPUB	2004/07/30 11:17

-	916	insert\$ near pipeline	USPAT;	2004/07/30 11:54
-	75	(insert\$ near pipeline) and stack	US-PGPUB USPAT;	2004/07/30 11:18
-	73	insert\$ near pipeline and interrupt	US-PGPUB USPAT;	2004/07/30 11:55
-	53	(insert\$ near pipeline and interrupt) not	USPAT;	2004/07/30 12:38
-	38	((insert\$ near pipeline) and stack)	US-PGPUB	
-		(insert\$ near pipeline and interrupt) not	USPAT;	2004/07/30 12:38
-		((insert\$ near pipeline) and stack) and	US-PGPUB	
-		processor		
-	1	("5867701").PN.	USPAT;	2004/08/09 08:15
-	33	("3911406"   "4038533"   "4050058"	US-PGPUB	
-		"4199811"   "4342078"   "4363091"	USPAT	2004/08/09 13:17
-		"4376976"   "4422144"   "4542453"		
-		"4610000"   "4654781"   "4751703"		
-		"4875160"   "4965724"   "5003462"		
-		"5005118"   "5101344"   "5129067"		
-		"5134701"   "5168571"   "5202967"		
-		"5210758"   "5212693"   "5233694"		
-		"5235686"   "5293592"   "5307478"		
-		"5335344"   "5371864"   "5452426"		
-		"5463745"   "5542059"   "5546552").PN.		
-	3	5867701.URPN.	USPAT	2004/08/09 08:15
-	1411477	A method a system and a computer program	EPO	2004/08/09 08:31
-		product for manipulating an instruction		
-		flow in a pipeline of a processor		
-	436145	(A method a system and a computer program	EPO	2004/08/09 08:31
-		product for manipulating an instruction		
-		flow in a pipeline of a processor).ttl.		
-	0	(method and system and computer and	EPO	2004/08/09 08:31
-		program and product and manipulating and		
-		instruction and pipeline and		
-		processor).ttl.		
-	0	method and system and computer and program	EPO	2004/08/09 08:32
-		and product and manipulating and		
-		instruction and pipeline and processor		
-	0	method and system and computer and program	EPO	2004/08/09 08:32
-		and product and manipulating and		
-		instruction and pipeline and processor		
-	0	method and system and computer and program	EPO	2004/08/09 08:35
-		and product and manipulating and		
-		instruction and pipeline		
-	1	method and system and computer and program	EPO	2004/08/09 08:44
-		and product and instruction and pipeline		
-	0	("1200425.5").PN.	EPO	2004/08/09 14:16
-	0	("1200425.5.an.").PN.	EPO	2004/08/09 08:38
-	0	("EP91401733").PN.	EPO	2004/08/09 08:41
-	0	("EP91401733.apn.").PN.	EPO	2004/08/09 08:41
-	1	EP91401733	EPO	2004/08/09 08:42
-	0	ep01200425.5	EPO	2004/08/09 08:48
-	0	ep01200425	EPO	2004/08/09 08:43
-	0	ep1200425	EPO	2004/08/09 08:43
-	0	ep12004255	EPO	2004/08/09 08:43
-	0	EP12004255	EPO	2004/08/09 08:43
-	1	computer and program and product and	EPO	2004/08/09 08:44
-		instruction and pipeline		
-	276	instruction and pipeline	EPO	2004/08/09 08:47
-	0	"Figure 1 resrepresents an architecture of a	EPO	2004/08/09 08:47
-		pipeline"		
-	0	"architecture of a pipeline"	EPO	2004/08/09 08:47
-	1	01200425.5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/09 08:50
-	0	20020116601.URPN.	USPAT	2004/08/09 08:49
-	0	2001EP-01200425.5	EPO	2004/08/09 08:50
-	323757	"2001" EP-01200425.5	EPO	2004/08/09 08:50
-	0	"2001" and EP-01200425.5	EPO	2004/08/09 08:50
-	0	EP-01200425.5	EPO	2004/08/09 08:50

-	0	EP01200425.5	EPO	2004/08/09 08:50
-	0	EP01200425	EPO	2004/08/09 08:50
-	0	EP012004255	EPO	2004/08/09 08:50
-	2099697	EP "01200425" (EP-463966-A2.DID. and (high-performance and multi-processor and floating and point and unit))	EPO	2004/08/09 08:51
-	2	EP and "01200425" and5	EPO	2004/08/09 08:51
-	0	EP and "01200425" and (EP-463966-A2.DID. and (high-performance and multi-processor and floating and point and unit))	EPO	2004/08/09 08:51
-	0	(WO99/18497).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/09 11:44
-	1	WO99/18497	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/09 11:40
-	0	WO9918497	EPO; JPO; DERWENT; IBM_TDB	2004/08/09 11:40
-	1446398	WO "9918497"	EPO; JPO; DERWENT; IBM_TDB	2004/08/09 11:42
-	1	WO and "9918497"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/09 11:46
-	2099697	"0" "463966" a2	EPO	2004/08/09 14:17
-	1	EP000463966a2	EPO	2004/08/09 11:54